

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al. Art Unit : 2673  
Serial No. : 09/504,235 Examiner : David L. Lewis  
Filed : February 15, 2000 Confirmation No.: 7610  
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

**MAIL STOP RCE**

Commissioner for Patents  
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INFORMATION DISCLOSURE STATEMENT


Applicants request consideration of the references listed on the attached PTO-1449 form. Copies of the listed U.S. patents or U.S. patent application publications can be provided upon request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: \_\_\_\_\_

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Reg. No. 37,640

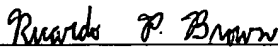
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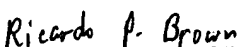
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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 07977-008004	Application No. 09/504,235
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Shunpei Yamazaki et al.	
		Filing Date February 15, 2000	Group Art Unit 2673

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation
	AX						Yes No

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Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AY							
	AZ							
	AAA							
	ABB							

Other Documents (include Author, Title, Date, and Place of Publication)		
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